

FIG. 1

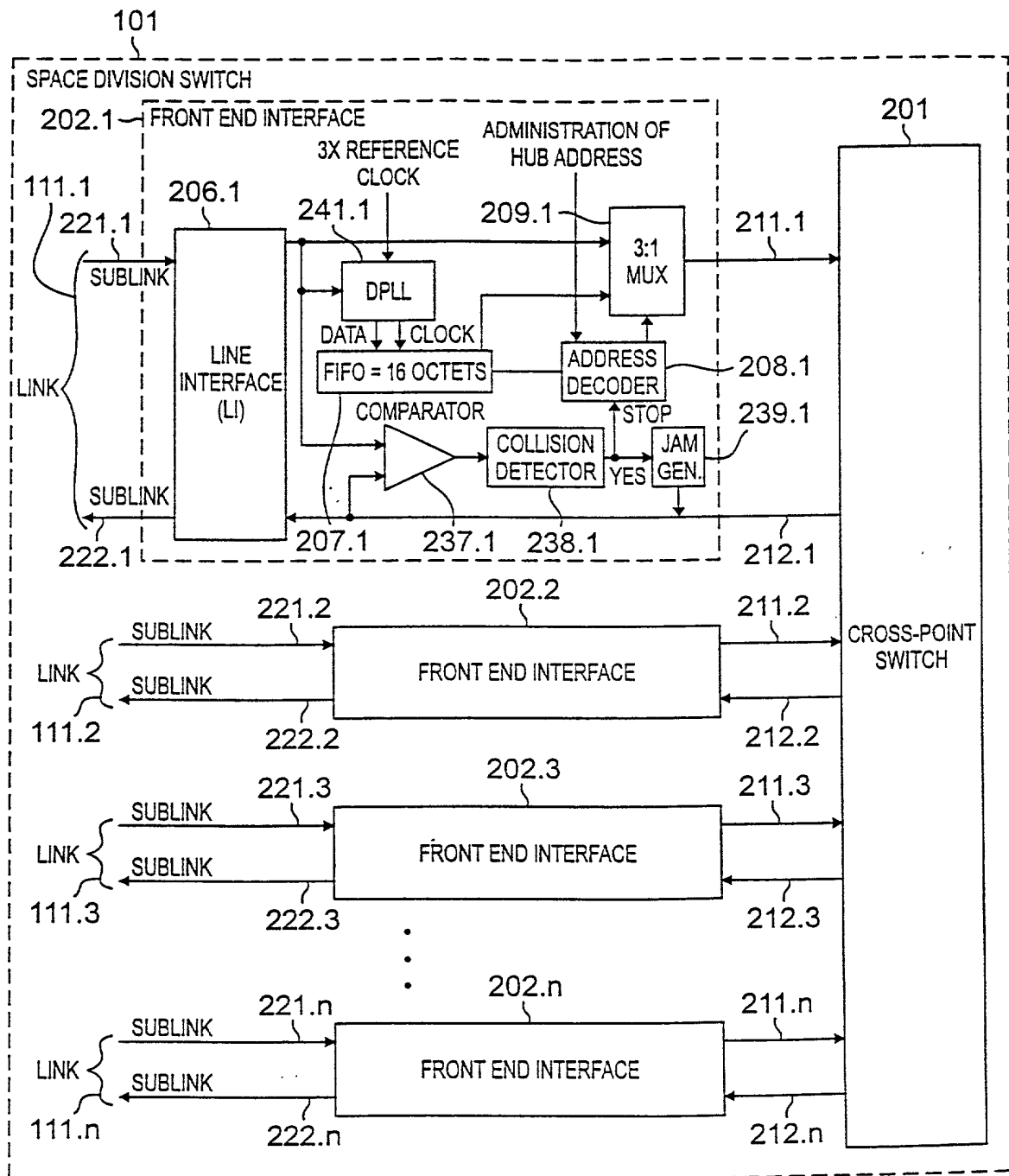


FIG. 2

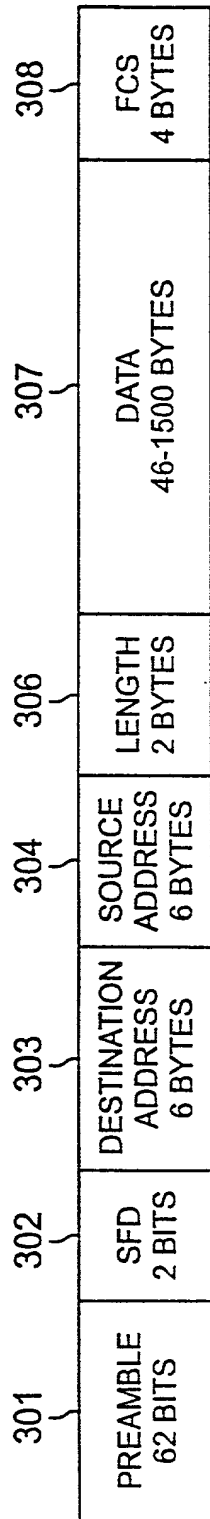


FIG. 3

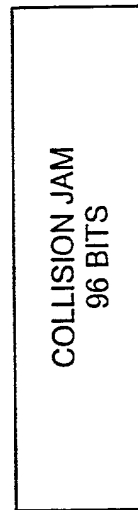


FIG. 4

The diagram illustrates a 3x3 optical switch architecture consisting of two stages, A and B, each with 3x3 input and output ports. Stage A has inputs labeled A(1,1) to A(3,3) and outputs labeled PORT 1 to PORT 3. Stage B has inputs labeled B(1,1) to B(3,3) and outputs labeled PORT 1 to PORT 3. Dashed lines represent the routing paths between the stages, showing a complex interconnection pattern.

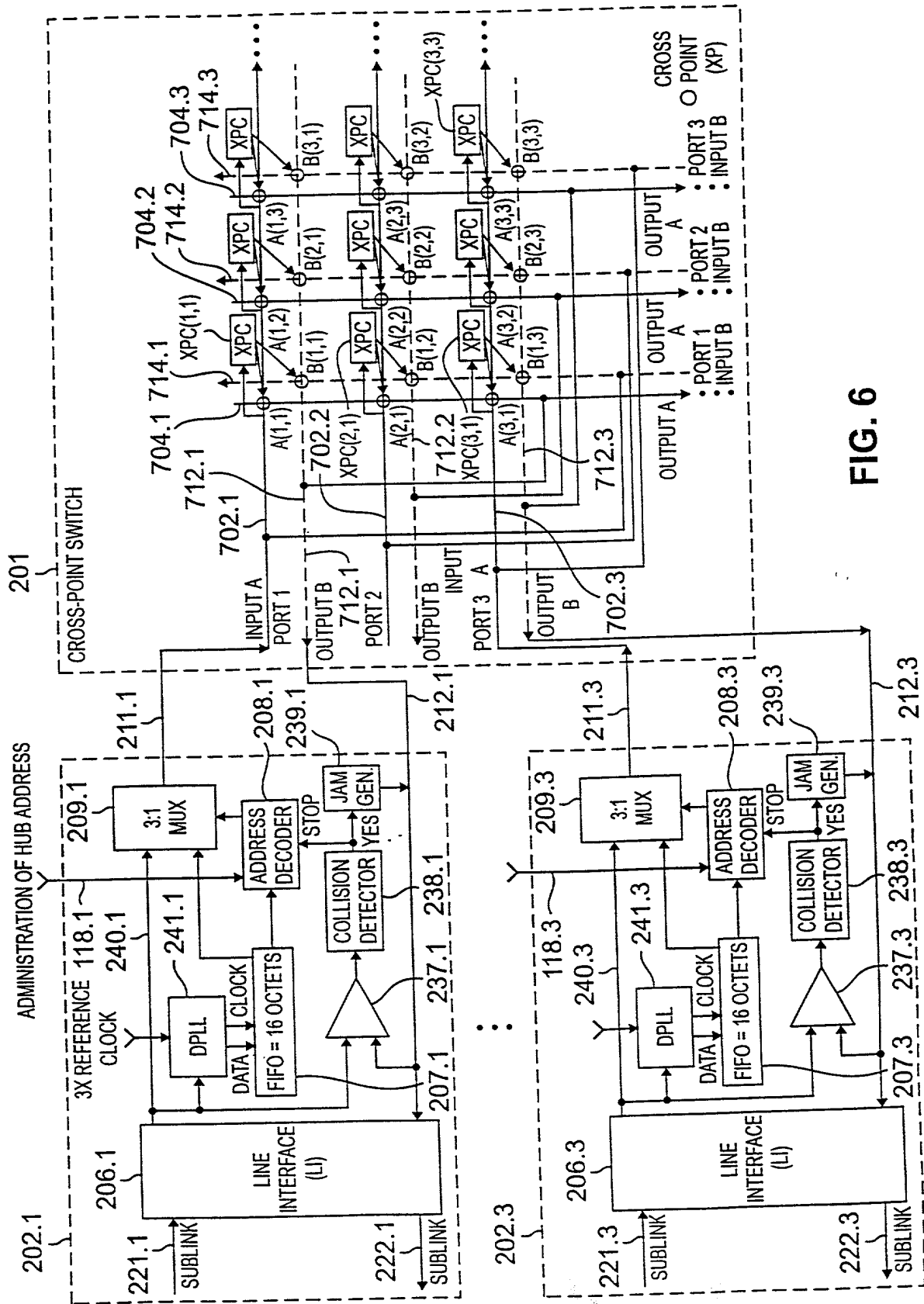


FIG. 6

4339000 906 1 300

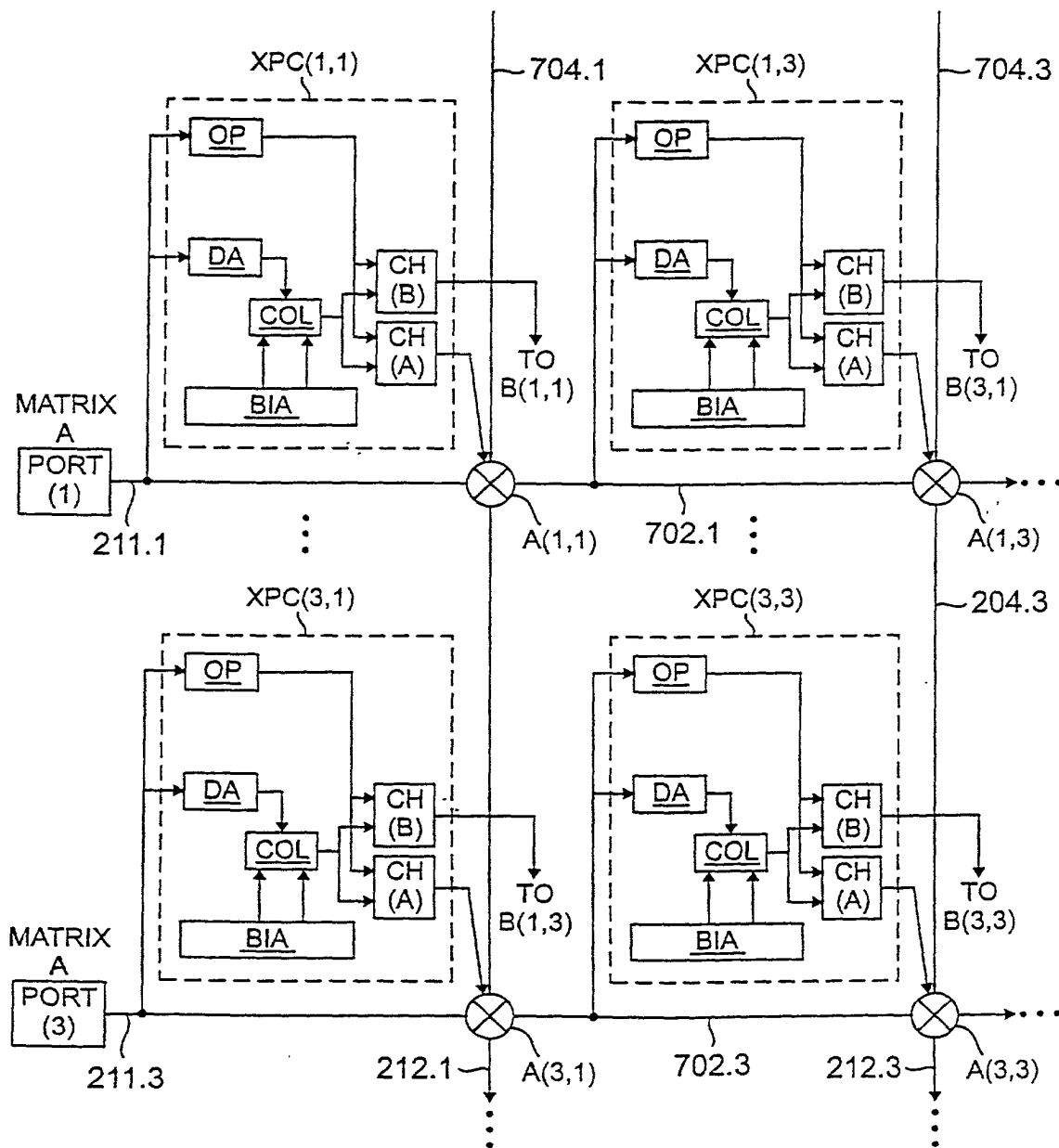
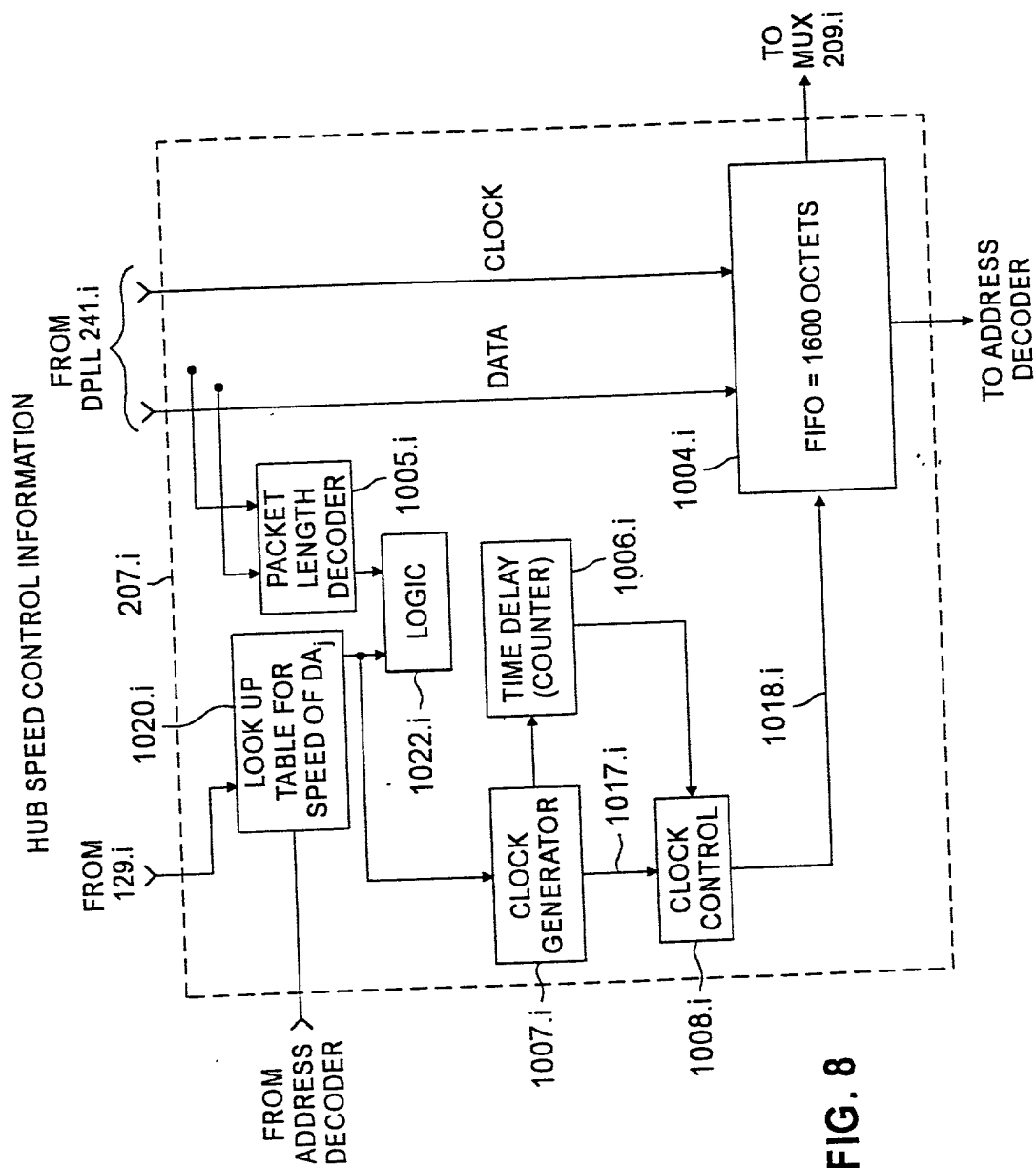


FIG. 7



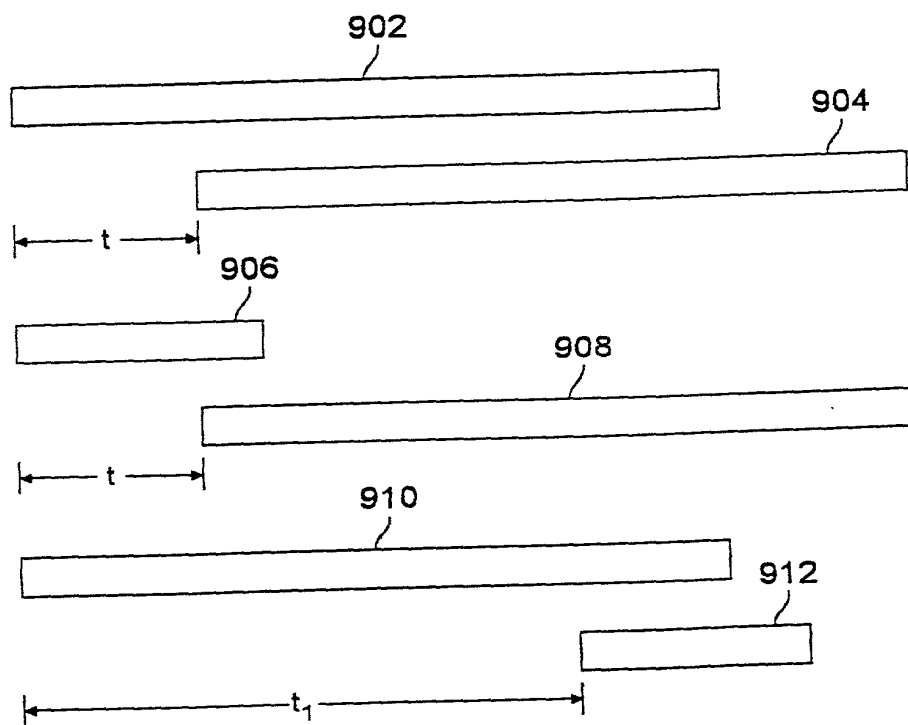
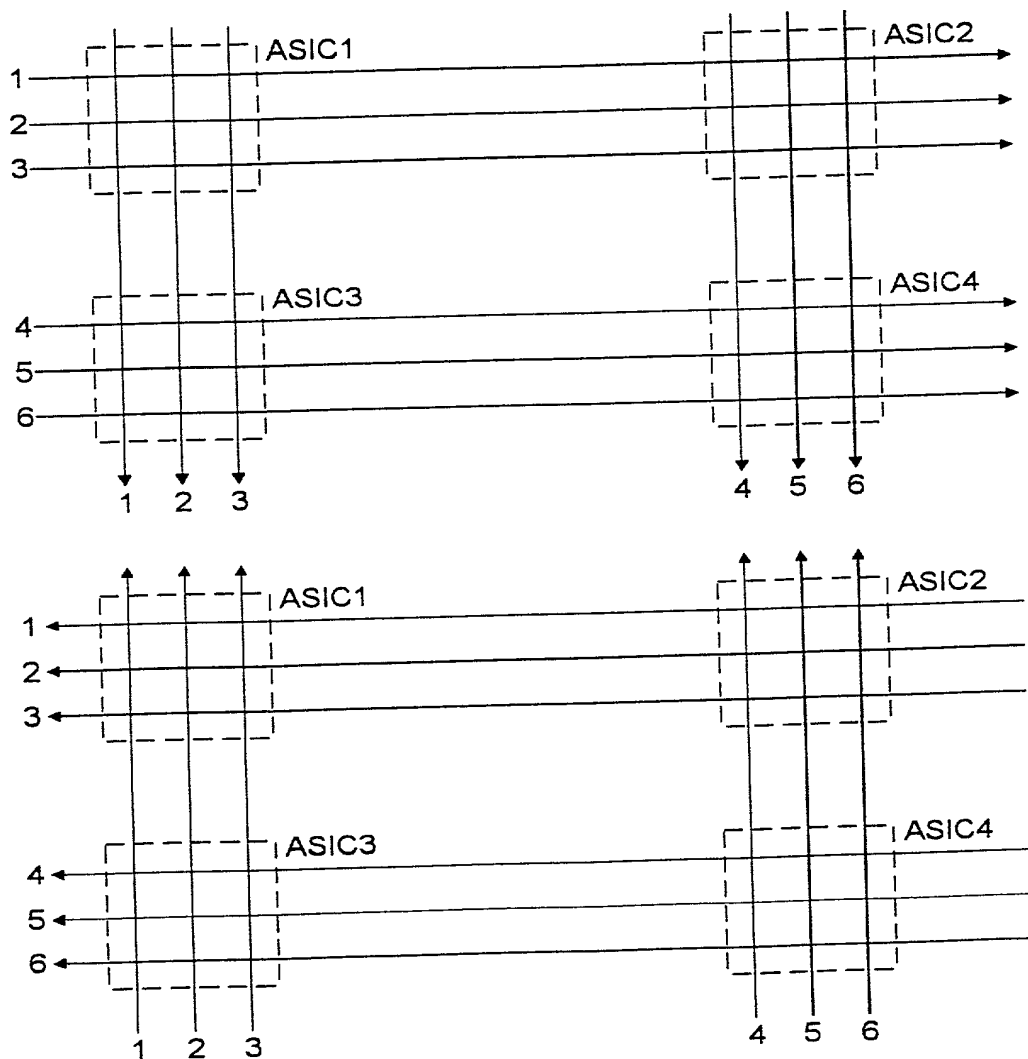


FIG. 9





**FIG. 10**